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0 results found in the Worldwide database for:

VRAM and DRAM and frame and partition in the title or abstract

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.....
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RESULT LIST

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VRAM and DRAM and rate and partition in the title or abstract

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RESULT LIST

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memory and frame and rate and partition in the title or abstract

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Applicant, Title of invention, Abstract --- e.g. computer semiconductor

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frame rate address memory

AND

AND

partition divide

OR

AND

AND

AND

Date of publication of application --- e.g. 19980401 - 19980405

-

AND

IPC --- e.g. D01B7/04 A01C11/02

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No.	Publication No.	Title
1.	<u>2007 - 213055</u>	METHOD OF TRANSFERRING FRAME DATA USING SYNCHRONOUS DYNAMIC RANDOM ACCESS MEMORY, METHOD OF TRANSFERRING FRAME DATA TO SOURCE DRIVER, AND TIMING CONTROL MODULE
2.	<u>2006 - 157152</u>	IMAGING APPARATUS AND METHOD
3.	<u>11 - 283015(1999)</u>	PICTURE COLOR MIXING PROCESSOR
4.	<u>11 - 234537(1999)</u>	FRAME DELAY DEVICE
5.	<u>09 - 322118(1997)</u>	INTERFACE CIRCUIT FOR DIGITAL VIDEO/AUDIO SIGNAL
6.	<u>09 - 093578(1997)</u>	IMAGE TRANSMITTER, IMAGE RECEIVER, IMAGE TRANSMISSION METHOD AND IMAGE RECEPTION METHOD
7.	<u>08 - 214169(1996)</u>	FRACTAL IMAGE CODING SYSTEM
8.	<u>07 - 079408(1995)</u>	PICTURE RECORDING AND REPRODUCING DEVICE
9.	<u>06 - 232893(1994)</u>	CELL RESOLVING MULTIPLEX PROCESSOR
10.	<u>05 - 328467(1993)</u>	TIME SWITCH CONSTITUTING SYSTEM
11.	<u>02 - 044860(1990)</u>	PICTURE DATA TRANSMISSION SYSTEM
12.	<u>63 - 107399(1988)</u>	TIME DIVISION SWITCH HOLDING MEMORY CONTROL SYSTEM
13.	<u>63 - 030099(1988)</u>	CHANNEL MEMORY OPERATION CONTROL SYSTEM FOR TIME DIVISION SWITCH
14.	<u>62 - 053096(1987)</u>	CONTROLLING SYSTEM FOR TIME SWITCH HOLDING MEMORY
15.	<u>61 - 245693(1986)</u>	DUPLEX STRUCTURE TIME DIVISION SWITCH
16.	<u>61 - 153780(1986)</u>	SEAL COLLATING DEVICE
17.	<u>60 - 133382(1985)</u>	ADDRESSING SYSTEM FOR DIGITAL SCAN CONVERTER OF ULTRASONIC DIAGNOSING APPARATUS
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double buffered rate frame display

AND

AND

partition divide

OR

AND

AND

AND

Date of publication of application --- e.g. 19980401 - 19980405

-

AND

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